

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re application of: Bailey et al.

Application No. 09/883,838

Filed: June 17, 2001

Confirmation No. 9970

For: SYNCHRONIZATION OF MULTIPLE
SIMULATION DOMAINS IN AN EDA
SIMULATION ENVIRONMENT

Examiner: Thomas H. Stevens

Art Unit: 2123

Attorney Reference No. 1011-64535-01

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INFORMATION DISCLOSURE STATEMENT PURSUANT TO
37 C.F.R. § 1.97(c)

Listed on the accompanying form PTO-1449 and enclosed herewith are several English-language documents. Applicants respectfully request that these documents be listed as references cited on the issued patent. This Information Disclosure Statement ("IDS") is being mailed before Applicants received a final action, a notice of allowance, or an action that otherwise closes prosecution in the referenced application.

Copies of United States patents and United States published patent applications do not have to be provided to the Patent Office (37 C.F.R. 1.98(a)(2)(ii)). Copies of unpublished U.S. applications do not have to be provided, as long as the application is available on PAIR, as this requirement of 37 C.F.R. § 1.98(a)(2)(iii) has been waived by the United States Patent and Trademark Office pursuant to the Official Gazette Notice on October 19, 2004 (1287 OG 163). Applicants will provide copies of such patents or applications upon request.

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Respectfully submitted,

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Docketing

INFORMATION DISCLOSURE STATEMENT BY APPLICANT

Attorney Docket Number	1011-64535-01
Application Number	09/883,838
Filing Date	June 17, 2001
First Named Inventor	Bailey
Art Unit	2123
Examiner Name	Thomas H. Stevens

U.S. PATENT DOCUMENTS

Copies of U.S. Patent documents do not need to be provided, unless requested by the Patent and Trademark Office. For patents, provide the patent number and the issue date. For published U.S. applications, provide the publication number and the publication date. For unpublished pending patent applications, provide the application number and the filing date.

Examiner's Initials*	Cite No. (optional)	Number	Publication Date	Name of Applicant or Patentee
		5872953	2/1992	Bailey, Brian
		6691301	2/2004	Bowen, Matt
		5995736	11/1999	Aleksic et al.
		6810373	10/2004	Harmon et al.
		5771370	6/1998	Klein, Russell
		6178542	1/2001	Dave, Bharat P.
		5960188	9/1999	Linke et al.
		6208954	3/2001	Houtchens, Steven P.
		6820243	11/2004	Shey et al.
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		5327361	7/1994	Long et al.
		5966537	10/1999	Ravichandran, Hari K.
		6983237	1/2006	Paterson et al.
		5119483	6/1992	Madden et al.

FOREIGN PATENT DOCUMENTS

Examiner's Initials*	Cite No. (optional)	Country	Number	Publication Date	Name of Applicant or Patentee
		Europe	EP0418980	3/27/1991	

EXAMINER
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DATE
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INFORMATION DISCLOSURE STATEMENT BY APPLICANT	Attorney Docket Number	1011-64535-01
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	First Named Inventor	Bailey
	Art Unit	2123
	Examiner Name	Thomas H. Stevens

Examiner's Initials*	Cite No. (optional)	OTHER DOCUMENTS
		Bailey, B., "Today's Problem - a real example" PowerPoint presentation, 1997 Mentor Graphics, pg. 1-6.
		Liao et al., "Towards a New Standard for System-Level Design" IEEE, Pg. 2-6, May 2000
		Opdahl et al., "Towards an Integrated, Simulative Framework for Real-World Modelling", 1996, pp. 515-524.
		Bailey et al., "Hardware/Software Co-Simulation Strategies for the Future", February 2000 Mentor Graphics Corporation, pp. 1-9.

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